

IN THE CLAIMS

1. (Currently Amended) An apparatus, comprising:

a plurality of inner firewalls comprising a hardware component and/or a firmware component configured to operate within a personal computer, the personal computer being configured to operate in a network of computers,

the personal computer comprising at least two microprocessors and at least two memory components, the at least two microprocessors being located on a single microchip;

at least one of the plurality of inner firewalls being configured to deny access to at least a first microprocessor and at least a first memory component of the personal computer by another computer through a network connection with the personal computer during a shared operation, and

at least one of the plurality of inner firewalls being configured to allow access to at least a second microprocessor and at least a second memory component of the personal computer by the other computer through the network connection with the personal computer during the shared operation.

2. (Currently Amended) The apparatus of claim 1, wherein a hardware component and/or software file, and/or firmware file is located within one of the inner firewalls.

3. (Currently Amended) The apparatus of claim 1, wherein ~~at least two of a hardware component, a software file, and a firmware file~~ is the following are grouped exclusively together inside one of the inner firewalls:

a hardware component and a software file;

a hardware component and a firmware file;

a software file and a firmware file; or

a hardware component, a software file, and a firmware file.

4. (Previously Presented) The apparatus of claim 1, wherein at least one of the inner firewalls is substantially a hardware component.

5. (Previously Presented) The apparatus of claim 1, wherein the personal computer comprises a dense wave division multiplexing (DWDM) network connection.

6. (Previously Presented) The apparatus of claim 1, wherein the personal computer comprises a wireless network connection.

7. (Previously Presented) The apparatus of claim 6, wherein the personal computer comprises a hardware encryption component.

8. (Currently Amended) The apparatus of claim 1, wherein the personal computer includes an operating system comprises comprising more than one independent component, one or more of the components having its own firewall.

9. (Currently Amended) The apparatus of claim 1, wherein the personal computer includes an operating system ~~comprises~~ comprising more than one independent component, one or more of the components having its own firewall.

10. (Currently Amended) The apparatus of claim 1, wherein the personal computer includes an application program [comprises] comprising more than one independent component, one or more of the components having its own firewall.

11. (Currently Amended) The apparatus of claim 1, wherein the personal computer includes an application program [comprises] comprising more than one independent components, one or more of the components having its own firewall.

12. (Currently Amended) The apparatus of claim 1, wherein all files of a network-accessible portion of volatile memory of the personal computer are erased when control of the network-accessible portion is transferred between the network of computers and a user of the personal computer, the network-accessible portion being located outside at least one of the inner firewalls.

13. (Previously Presented) The apparatus of claim 12, wherein the file erasure is accomplished by one of power interruption and overwriting.

14. (Currently Amended) The apparatus of claim 1, wherein all files in a network-accessible portion of a non-volatile memory of the personal computer are erased when control of the network-accessible portion is transferred between the network and a user of the personal computer, [said] the network-accessible portion being located outside at least one of the inner firewalls.

15. (Previously Presented) The apparatus of claim 14, wherein the at least one inner firewall is located on the microchip.

16. (Previously Presented) The apparatus of claim 1, wherein the first memory component comprises a system BIOS.

17. (Previously Presented) The apparatus of claim 18, wherein the microchip comprises an encryption component.

18. (Currently Amended) The apparatus of claim 1, wherein the microchip [having] has at least four or 8 or 16 or 32 or 64 or 128 or 256 or 512 or 1024 microprocessors.

19. (Previously Presented) The apparatus of claim 18, wherein the microchip comprises a field-programmable gate array (FPGA) and/or active configuration of an integrated circuit.

20. (Previously Presented) The apparatus of claim 1, wherein the network of computers comprises a World Wide Web and/or an Internet.

21. (Previously Presented) The apparatus of claim 1, wherein the network connection comprises an optical fiber connection substantially directly to the personal computer.

22. (Previously Presented) The apparatus of claim 1, wherein the first memory component is a flash memory device.

23. (Previously Presented) The apparatus of claim 1, wherein the second memory component is a flash memory device.

24. (Previously Presented) The apparatus of claim 1, wherein the second memory component is a random access memory (RAM) device.

25. (Previously Presented) The apparatus of claim 1, wherein the second memory component is a hard drive device.

26. (Previously Presented) The apparatus of claim 1, wherein the second memory component is a read-only compact disk drive (CD-ROM) device.

27. (Previously Presented) The apparatus of claim 1, wherein the second memory component is a read-only digital video disk drive (DVD) device.

28. (Previously Presented) The apparatus of claim 1, wherein the second memory component is volatile memory.

29. (Previously Presented) The apparatus of claim 1, wherein the second memory component is non-volatile memory.

30. (Previously Presented) The apparatus of claim 29, wherein the non-volatile memory comprises a magnetic random access memory (MRAM) and/or ovonic memory.

31. (Previously Presented) The apparatus of claim 1, wherein the first memory component is non-volatile memory.

32. (Previously Presented) The apparatus of claim 1, wherein the second memory component duplicates a first memory component.

33. (Previously Presented) The apparatus of claim 1, wherein the first memory component is read and write memory.

34. (Previously Presented) The apparatus of claim 1, wherein the second memory component is read-only memory.

35. (Previously Presented) An apparatus, comprising:
a plurality of inner firewalls configured to operate within a personal computer,
the personal computer being configured to operate in a network of computers,
the personal computer comprising at least two microprocessors,
at least one of the plurality of inner firewalls being configured to deny access to at
least a first microprocessor of the personal computer by another computer through a
network connection with the personal computer during a shared operation, and
at least one of the plurality of inner firewalls being configured to allow access to
at least a second microprocessor of the personal computer by the other computer through
the network connection with the personal computer during the shared operation.

36. (Previously Presented) An apparatus, comprising:
a plurality of inner firewalls configured to operate within a personal computer, the
personal computer being configured to operate in a network of computers,
the personal computer comprising at least two microprocessors and at least two
memory components,
at least one of the plurality of inner firewalls being configured to deny access to at
least a first microprocessor and at least a first memory component of the personal
computer by another computer through a network connection with the personal computer
during a shared operation, and
at least one of the plurality of inner firewalls being configured to allow access to
at least a second microprocessor and at least a second memory component of the personal

computer by the other computer through the network connection with the personal computer during the shared operation.

37. (Previously Presented) The apparatus of claim 35, wherein at least one of the inner firewalls comprises a hardware component and/or a firmware component.

38. (Previously Presented) The apparatus of claim 36, wherein at least one of the inner firewalls comprises a hardware component and/or a firmware component.

39. (Previously Presented) The apparatus of claim 37, wherein at least two or four or 8 or 16 or 32 or 64 or 128 or 256 or 512 or 1024 microprocessors are located on a single microchip.

40. (Previously Presented) The apparatus of claim 38, wherein at least two or four or 8 or 16 or 32 or 64 or 128 or 256 or 512 or 1024 microprocessors are located on a single microchip.

41. (Previously Presented) The apparatus of claim 39, wherein the microchip comprises an encryption component.

42. (Previously Presented) The apparatus of claim 40, wherein the microchip comprises an encryption component.

43. (Previously Presented) The apparatus of claim 39, wherein the microchip comprises a field-programmable gate array (FPGA) and/or active configuration of an integrated circuit.

44. (Previously Presented) The apparatus of claim 40, wherein the microchip comprises a field-programmable gate array (FPGA) and/or active configuration of an integrated circuit.

45. (Previously Presented) The apparatus of claim 39, wherein the microchip comprises a master control and/or processing microprocessor.

46. (Previously Presented) The apparatus of claim 40, wherein the microchip comprises a master control and/or processing microprocessor.

47. (Previously Presented) The apparatus of claim 18, wherein the microchip comprises a master control and/or processing microprocessor.

48. (Previously Presented) The apparatus of claim 39, wherein the personal computer comprises a wireless network connection.

49. (Previously Presented) The apparatus of claim 40, wherein the personal computer comprises a wireless network connection.